

| Ref # | Hits | Search Query   | DBs   | Default Operator | Plurals | Time Stamp       |
|-------|------|--|---|------------------|---------|------------------|
| L1    | 8    | cancellation adj circuit and controller and uncorrelat\$4 and adjust\$4                | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/10/30 13:12 |
| L2    | 328  | cancellation adj circuit and controller and adjust\$4                                  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/10/30 13:12 |
| L3    | 8    | cancellation adj circuit and controller and adjust\$4 and distort\$4 and uncorrelat\$4 | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/10/30 13:12 |
| L4    | 155  | cancellation adj circuit and controller and adjust\$4 and distort\$4                   | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/10/30 13:12 |
| L5    | 173  | L2 not L4  | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/10/30 13:12 |
| L6    | 6    | "341"/\$.ccls. and (cancel\$7 adj circuit and controller and adjust\$4)                | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/10/30 13:14 |
| L7    | 2    | "341"/\$.ccls. and (cancellation adj circuit and controller and adjust\$4)             | US-PGPUB;<br>USPAT;<br>USOCR;<br>EPO; JPO;<br>DERWENT;<br>IBM_TDB | OR               | ON      | 2005/10/30 13:14 |